



Our Reference: DFS-138-A

2841

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Martin Gehrke & Torsten Pechstein
Serial Number: 10/001,668
Filing Date: October 23, 2001
Examiner/Art Group Unit: Unknown/2841
Title: A DESIGN FOR CONSTRUCTING AN INPUT CIRCUIT TO RECEIVE AND PROCESS AN ELECTRICAL SIGNAL

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CERTIFICATE OF MAILING AND TRANSMITTAL LETTER

Assistant Commissioner of Patents
Washington, D.C. 20231

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Sir:

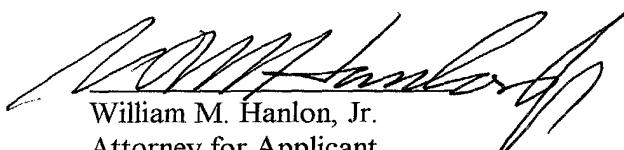
OFFICE OF PETITIONS

Transmitted with this document is a Postcard; Information Disclosure Statement including Form PTO-1449 and the cited references in the above-identified application.

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I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner of Patents, Washington, D.C. 20231, on July 16, 2002.


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A DESIGN FOR CONSTRUCTING AN INPUT
CIRCUIT TO RECEIVE AND PROCESS AN
ELECTRICAL SIGNAL

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner of Patents
Washington, D.C. 20231

Sir:

Applicant(s) hereby cite the references listed on the attached form PTO-1449 as relating to the subject matter of the invention described and claimed in the above-identified application.

A copy of an English language translation of the cited foreign reference is attached to the copy of the cited reference submitted herewith.

Respectfully submitted,

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MacFARLANE, WOOD & HELMHOLDT,
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Dated: July 16, 2002

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